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Bib Data Sheet

CONFIRMATION NO. 5713

SERIAL NUMBER 09/836,065	FILING DATE 04/16/2001 RULE	CLASS 324	GROUP ART UNIT 2133	ATTORNEY DOCKET NO. P1310	
APPLICANTS Janevoot Naksrikram, Bangkok, THAILAND; Aeksit Suraphak, Sunnyvale, CA; Jitrayut Junnapart, Nonthaburi, THAILAND;					
** CONTINUING DATA ***** <i>PCT/US01/4342</i>					
** FOREIGN APPLICATIONS ***** <i>JP</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/11/2001					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Met after met <i>Allowance</i> Verified and Acknowledged <i>Examiner's Signature</i> <i>Initials</i>		STATE OR COUNTRY THAILAND	SHEETS DRAWING 2	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 2
ADDRESS Victor Flores LaRiviere, Grubman & Payne, LLP P.O. Box 3140 Monterey, CA 93942					
TITLE System and method for erase test of integrated circuit device having non-homogeneously sized sectors					
FILING FEE RECEIVED 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		



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Typed or Printed Name of Person Mailing Paper or Fee: Patricia Aguillon

Signature: Patricia Aguillon

PATENT
Docket No. P1310

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APPLICANTS: JANEVOOT NAKSRIKRAM
AEKSIT SURAPHAK
JITRAYUT JUNNAPART

SERIAL NO.: 09/836,065 EXAMINER: UNKNOWN

FILED: APRIL 16, 2001 ART UNIT: 2858

FOR: SYSTEM AND METHOD FOR ERASE TEST OF INTEGRATED
CIRCUIT DEVICE HAVING NON-HOMOGENEOUSLY SIZED
SECTORS

OFFICE OF INITIAL PATENT EXAMINATION
CUSTOMER SERVICE CENTER
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WASHINGTON, D.C. 20231

TRANSMITTAL LETTER

Dear Sir:

In connection with the above-referenced patent application, transmitted herewith are the following:

1. Request for Corrected Filing Receipt (2 pages);
2. Copy of Filing Receipt with corrections marked in red ink (1 page);
3. Status Letter (2 pages);
4. Appointment of Associate Counsel (2 pages); and
5. Post card in acknowledgment of receipt of all transmitted materials.

F1024

Please date-stamp the enclosed post cards and return same to the undersigned in acknowledgment of receipt of all transmitted material.

Respectfully submitted,



Don R. Mollick
Reg. No. 28,972

DRM/taa/kgl

OCTOBER 18, 2001

LARIVIERE, GRUBMAN & PAYNE, LLP
Post Office Box 3140
Monterey, CA 93942
(831) 649-8800



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Typed or Printed Name of Person Mailing Paper or Fee: Patricia Aguillon

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REQUEST FOR CORRECTED FILING RECEIPT

1. Attached is a copy of the first page of the Filing Receipt for which issuance of a corrected filing receipt is respectfully requested.

2. There is an error in:

X Applicant's name and/or address, in that it should read as follows:

Aeksit Suraphak, Sunnyvale, CA

____ Title

____ Filing Date

Serial Number

Foreign/PCT Application

Attorney's Address, in that it should read as follows:

LARIVIERE, GRUBMAN & PAYNE, LLP
P.O. BOX 3140
MONTEREY, CA 93942

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in acknowledgment of receipt of all transmitted material.

Respectfully submitted,



Don R. Mollick
Reg. No. 28,972

DRM/taa/kgl

OCTOBER 18, 2001

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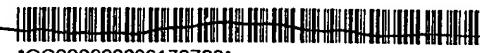
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/836,065	04/16/2001	2858	710	P1310	2	10	2

CONFIRMATION NO. 5713

FILING RECEIPT



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JAN 16 2002

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Date Mailed: 06/12/2001

Victor Flores
LaRiviere, Grubman & Payne, LLP
P.O. Box 3453 3140
Monterey, CA 93942

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Janevoot Naksrikram, Bangkok, THAILAND;
Aeksit Suraphak, Sunnyvale, CA; Sunnyvale
Jitrayut Junnapart, Nonthaburi, THAILAND;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 06/11/2001

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

System and method for erase test of integrated circuit device having non-homogeneously sized sectors



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STATUS LETTER

Dear Sir:

Please advise as to the status of this application. In particular, please inform us as to when the Examiner expects to take this case up for action. The most recent document received

from the Patent Office is the Notice of Recordation of Assignment issued on July 6, 2001.

Respectfully submitted,



Don R. Mollick
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OCTOBER 18, 2001

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